



2811

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Fernando Gonzalez  
Title: VERTICAL TRANSISTOR AND METHOD OF MAKING

Docket No.: 303.776US1  
Filed: August 30, 2001  
Examiner: Quang Vu

Serial No.: 09/945,495  
Due Date: February 18, 2003 (PTO Closed)  
Group Art Unit: 2811

Commissioner for Patents  
Washington, D.C. 20231

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By: Daniel J. Kluth  
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Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 19 day of February, 2003.

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